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Appl. No. 10/820,500  
Reply to Office action of December 26, 2007Amendments to the Specification

Please replace the paragraph [0029] in the specification with the following amended paragraph:

5

[0029] FIG. 5A is a block diagram showing a spread spectrum circuit using phase modulation technology according to an embodiment of the invention. The circuit includes a multi-phase oscillation clock generator 51, and a spread spectrum control circuit 50 comprising a modulation value generator 54, a Delta-Sigma modulator 53, a  
10 phase modulator 52, a divider 55, and a second phase-locked loop (PLL) 57. The multi-phase oscillation clock generator 51 may be a phase-locked loop (PLL), which is composed of a phase frequency detector (PFD) 511, a charge pump 512, a loop filter 513, a voltage controlled oscillator (VCO) 514, and a divider 515.

15